

### REMARKS/ARGUMENTS

Claims 1 and 3-6 are pending in the application. The claims have been amended; reexamination and reconsideration are hereby requested.

Claim 1 was rejected as anticipated by Safai. The Examiner pointed to image processor 310 and microprocessor 312.

Applicants reply that claim 1 has been amended to include the limitations of claim 2.

Claims 2-3 and 6 were rejected as unpatentable over Safai in view of Mizutani. With regard to claim 2, the Examiner applied Safai as before and cited Mizutani Figure 7 multipliers 85-87, 95-97 and adders 88-90, 98-100. With regard to claim 3, the Examiner applied Safai as before cited Mizutani JPEG 29.

With regard to claim 2 (amended claim 1), applicants reply that the cited multipliers and adders of Mizutani do not forma multiply and accumulate units because there is no accumulation.

With regard to claim 3, applicants reply that amended claim 3 requires the compression be of the acquired image, not of the already-processed image as in Mizutani which applies input processing 21 of Fig.2 prior to JPEG compression.

With regard to claim 6, applicants rely upon the patentability of parent claim 1.

Claims 4-5 were rejected as unpatentable over Safai in view of Fukuoka. The Examiner cited Fukuoka processing circuit 4 in addition to Safai as before.

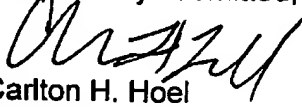
Applicants reply that amended claim 4 requires a digital image processing unit, whereas Fukuoka processing circuit 4 is analog.

With regard to claim 5, applicants rely upon the patentability of parent claim 1.

Proposed drawing corrections are in the Appendix.

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